

SUBJECT: NO DIGITAL RECORDING

DATE: APRIL 1997

MODEL (S): MZ-R3

MT-MZR3-109

FOR INTERNAL USE ONLY

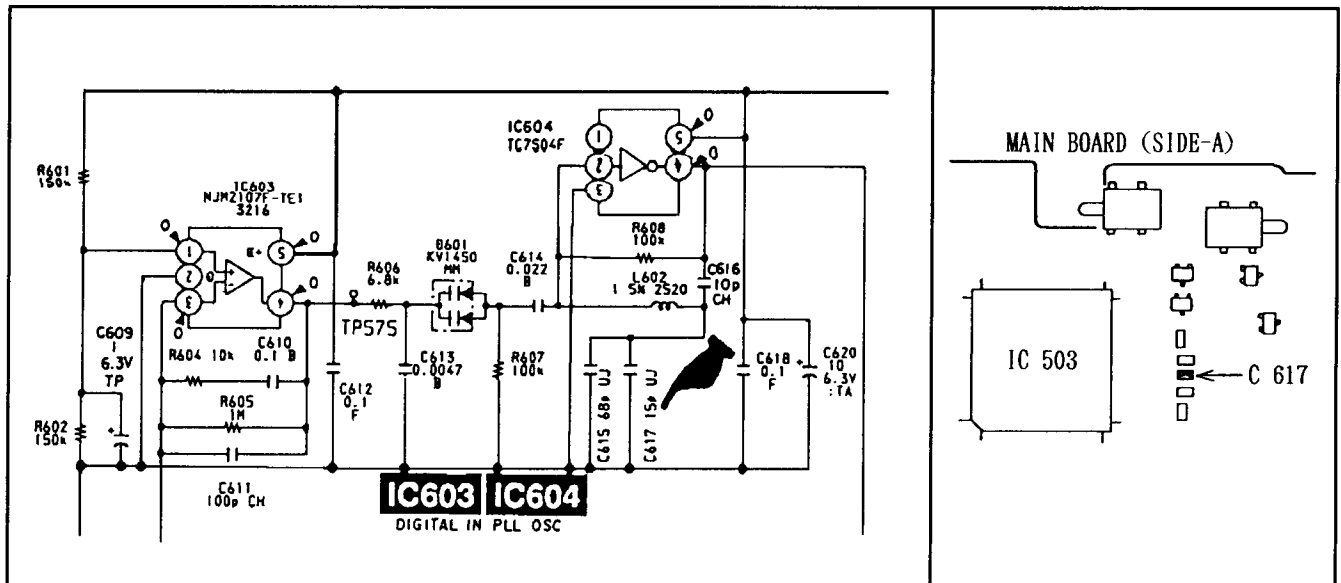
CAUSE:

Narrow digital PLL locking range.

SOLUTION:

- Confirm whether the voltage at IC603 pin 4 on the Main Board ranges between 1.5V and 2V when a digital signal is input.
- If the voltage is smaller than 1.5V, replace C617 chip 15pF on the A side of the Main Board with chip 10pF.

Ref.	Former	New
C617	1-164-118-11 (15PF)	1-162-997-11 (10PF)



GA06296
GA96035